Unit in mm

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

MT6L04AE

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

 Two devices are built in to the super-thin and extreme super mini (6 pins) package: ES6

MOUNTED DEVICES

| | Q1/Q2:SSM (TESM) |
|---|---------------------|
| Three-pins (SSM/TESM) mold products are corresponded. | MT3S04AS (MT3S04AT) |

MAXIMUM RATINGS (Ta = 25°C)

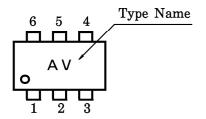
| CHARACTERISTIC | SYMBOL | Q1/Q2 | UNIT |
|-----------------------------|-------------------------|---------|------|
| Collector-Base Voltage | v_{CBO} | 10 | V |
| Collector-Emitter Voltage | v_{CEO} | 5 | V |
| Emitter-Base Voltage | $V_{ m EBO}$ | 2 | V |
| Collector Current | $I_{\mathbf{C}}$ | 40 | mA |
| Base Current | $I_{\mathbf{B}}$ | 10 | mA |
| Collector Power Dissipation | P _C (Note 1) | 100 | mW |
| Junction Temperature | T_{j} | 125 | °C |
| Storage Temperature Range | $\mathrm{T_{stg}}$ | -55~125 | °C |

(Note 1): Total power dissipation of Q1 and Q2.

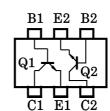
1. COLLECTOR 1 4. BASE 2 2. EMITTER 1 5. EMITTER 2 3. COLLECTOR 2 6. BASE 1 JEDEC — JEITA — TOSHIBA 2-2N1C

Weight: 0.003 g

MARKING



PIN ASSIGNMENT (TOP VIEW)



2001-12-10

ELECTRICAL CHARACTERISTICS Q1/Q2 (Ta = 25°C)

| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|---------------------------------|--------------------|---|------|------|------|---------|
| Collector Cut-off Current | I_{CBO} | $V_{CB} = 5 V, I_{E} = 0$ | _ | _ | 0.1 | μ A |
| Emitter Cut-off Current | I_{EBO} | $V_{EB} = 1 V, I_{C} = 0$ | _ | _ | 1 | μ A |
| DC Current Gain | $h_{	extbf{FE}}$ | $V_{CE} = 1 V$, $I_{C} = 5 mA$ | 80 | _ | 160 | _ |
| Transition Frequency | f _T (1) | $V_{CE} = 1 V$, $I_{C} = 5 mA$ | 2 | 4.5 | _ | GHz |
| | f _T (2) | $V_{ m CE}=3~{ m V},~{ m I}_{ m C}=7~{ m mA}$ | 5 | 7 | _ | GHz |
| Insertion Gain | $ S_{21e} ^2(1)$ | $V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA},$ f = 1 GHz | _ | 8.5 | _ | dB |
| | $ S_{21e} ^2$ (2) | $V_{CE} = 3 \text{ V}, I_{C} = 20 \text{ mA}, $ f = 1 GHz | 7.5 | 11 | _ | dB |
| Noise Figure | NF (1) | $V_{\text{CE}} = 1 \text{ V}, \text{ I}_{\text{C}} = 5 \text{ mA},$ $f = 1 \text{ GHz}$ | | 1.3 | 2.2 | dB |
| | NF (2) | $V_{\text{CE}} = 3 \text{ V}, \text{ I}_{\text{C}} = 7 \text{ mA},$ $f = 1 \text{ GHz}$ | | 1.2 | 2 | dB |
| Reverse Transfer Capacitance | $\mathrm{C_{re}}$ | $V_{CB} = 1 V, I_{E} = 0,$ f = 1 MHz (Note 2) | _ | 0.9 | 1.25 | pF |

(Note 2): C_{re} is measured by 3 terminal method with capacitance bridge.

HANDLING PRECAUTION

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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